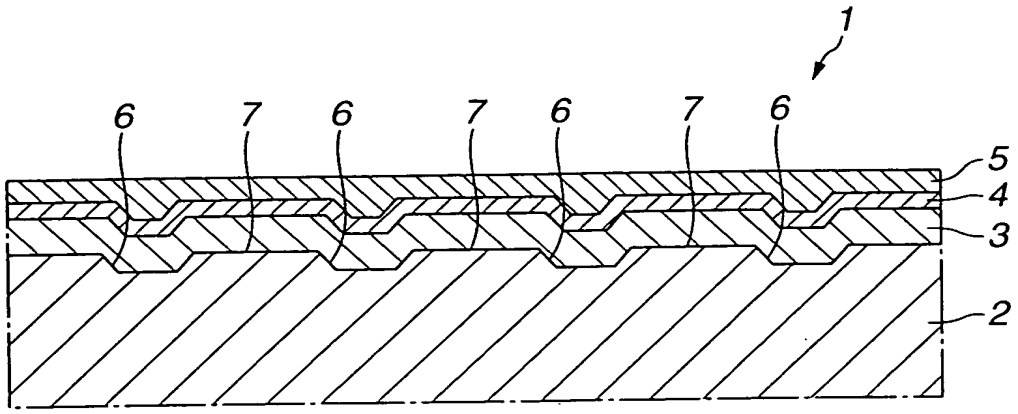


FIG.1

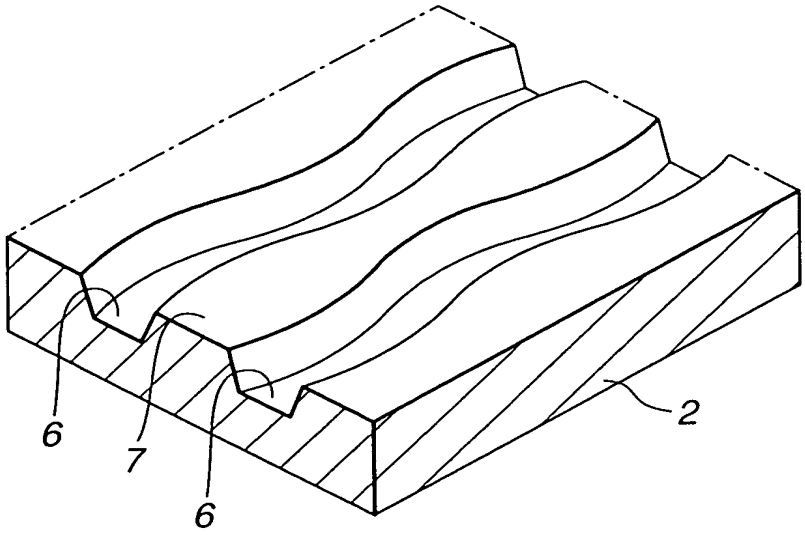


FIG.2

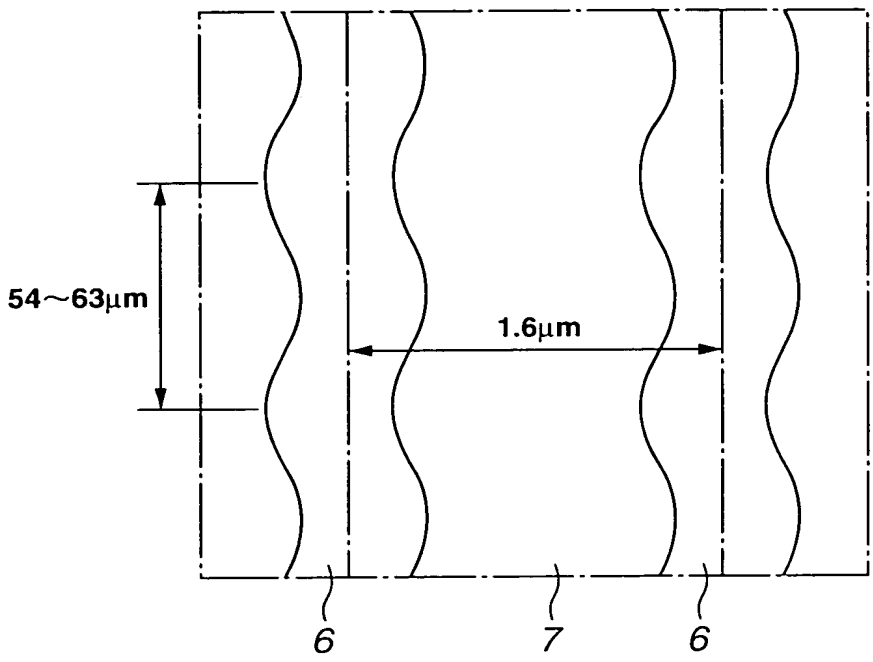


FIG.3

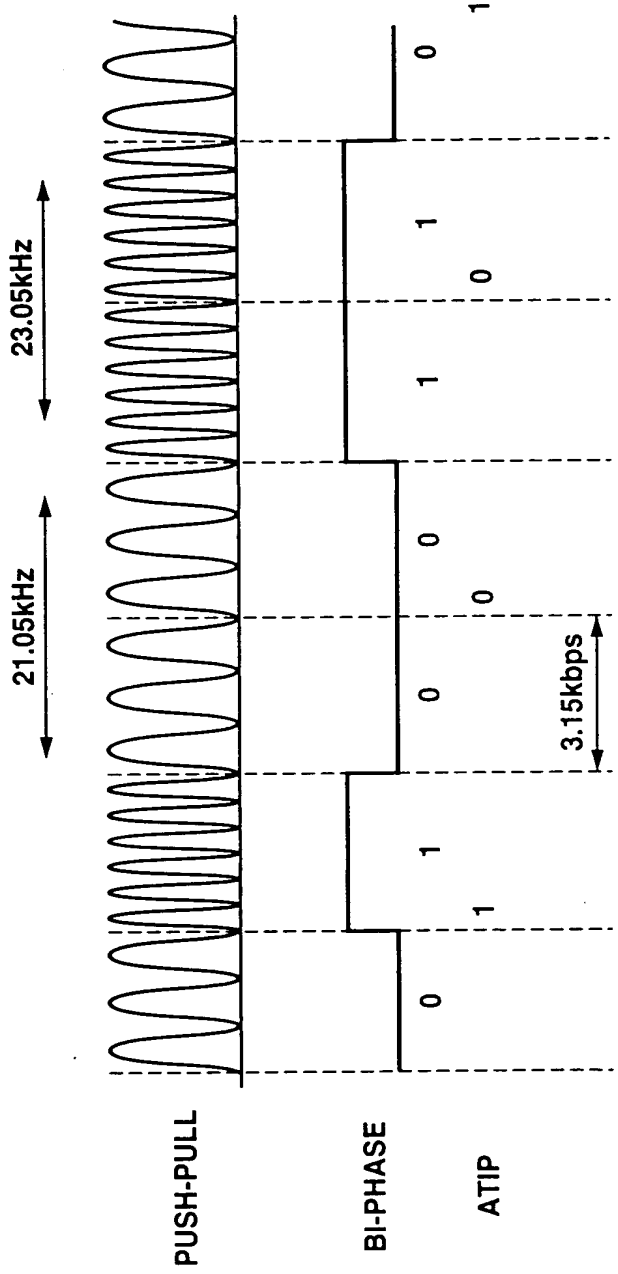

23.05kHz
21.05kHz
3.15kbps
PUSH-PULL
BI-PHASE
ATIP
0
1
1
0
1
0
0
0
1
0
1

FIG.4

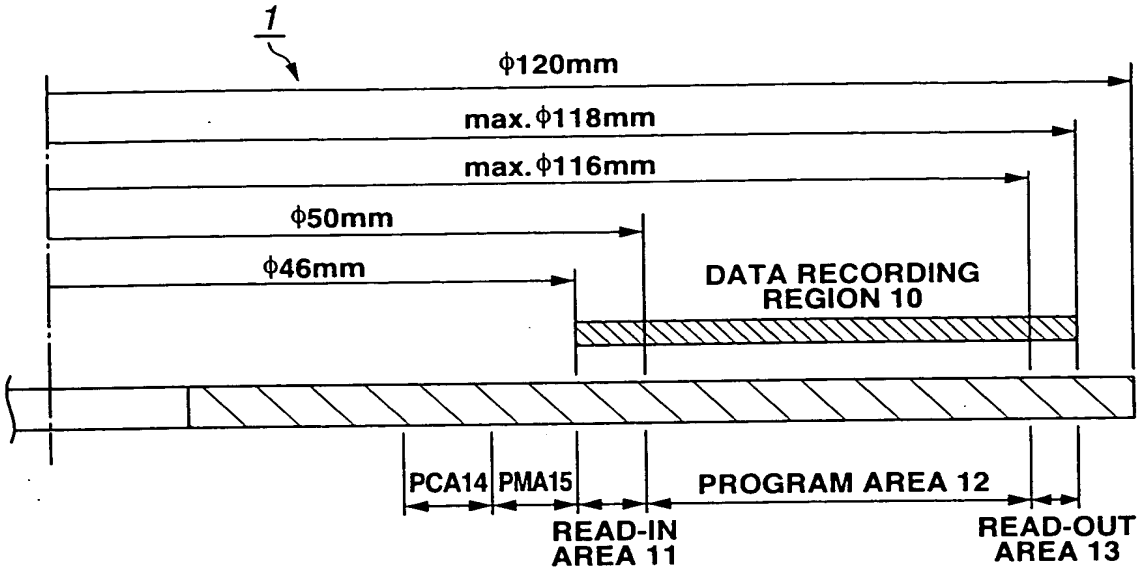


FIG.5

FIG.6A

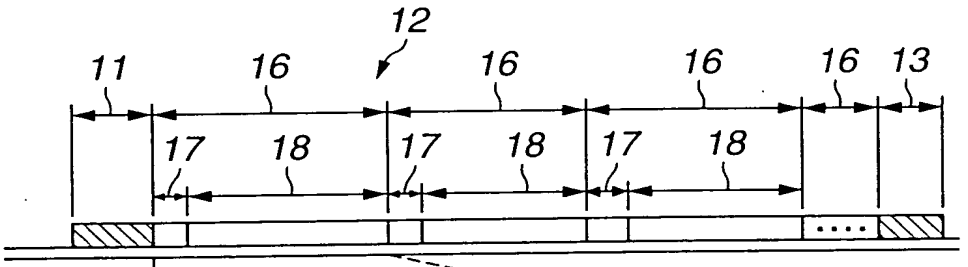


FIG.6B

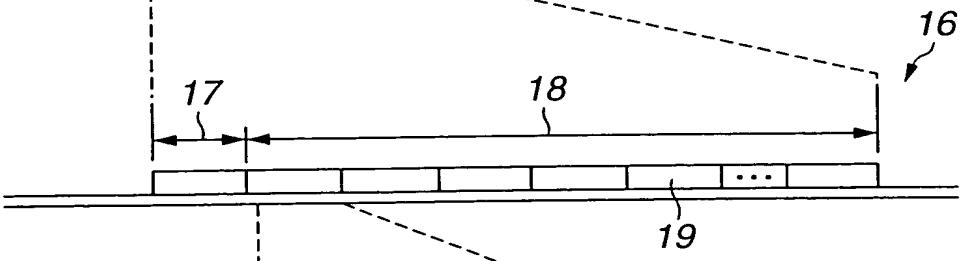
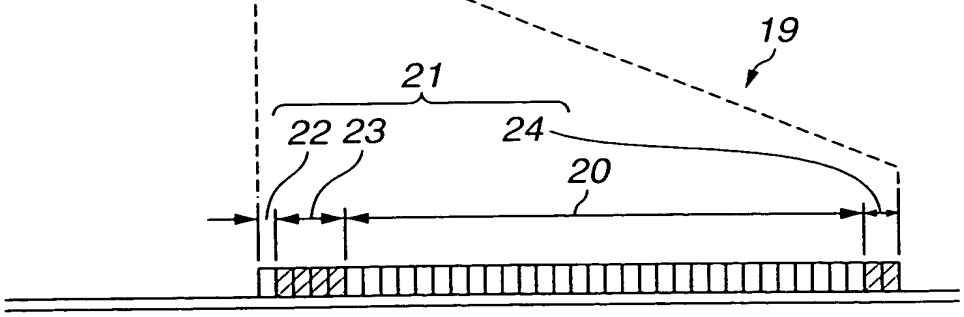


FIG.6C



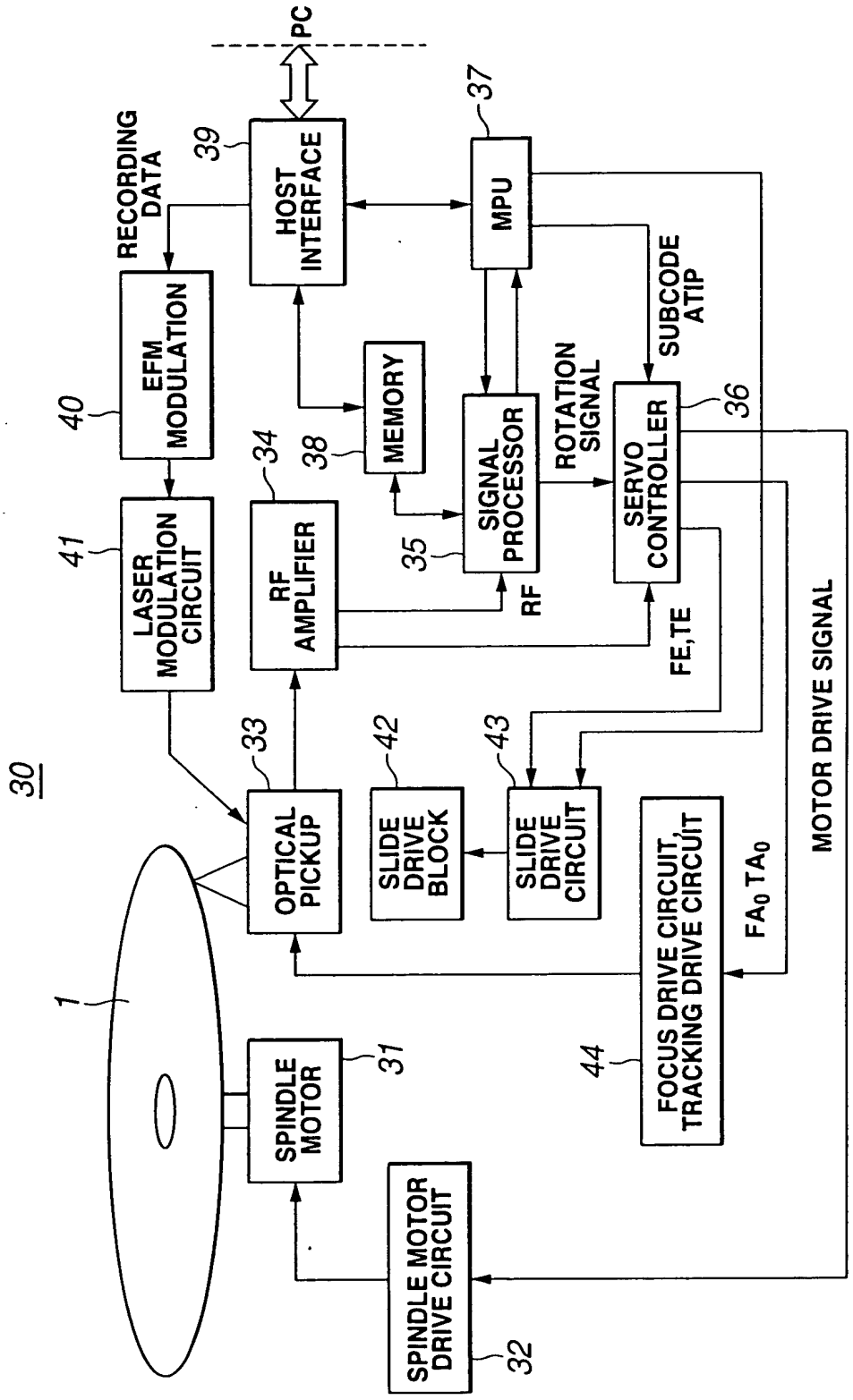

30
PC
RECORDING DATA
39
HOST INTERFACE
37
MPU
SUBCODE ATIP
40
EFM MODULATION
38
MEMORY
34
SIGNAL PROCESSOR
ROTATION SIGNAL
SERVO CONTROLLER
36
41
LASER MODULATION CIRCUIT
RF AMPLIFIER
35
RF
FE,TE
33
42
43
OPTICAL PICKUP
SLIDE DRIVE BLOCK
SLIDE DRIVE CIRCUIT
MOTOR DRIVE SIGNAL
1
FOCUS DRIVE CIRCUIT, TRACKING DRIVE CIRCUIT
FA_0 TA_0
44
SPINDLE MOTOR
31
SPINDLE MOTOR DRIVE CIRCUIT
32

FIG.7

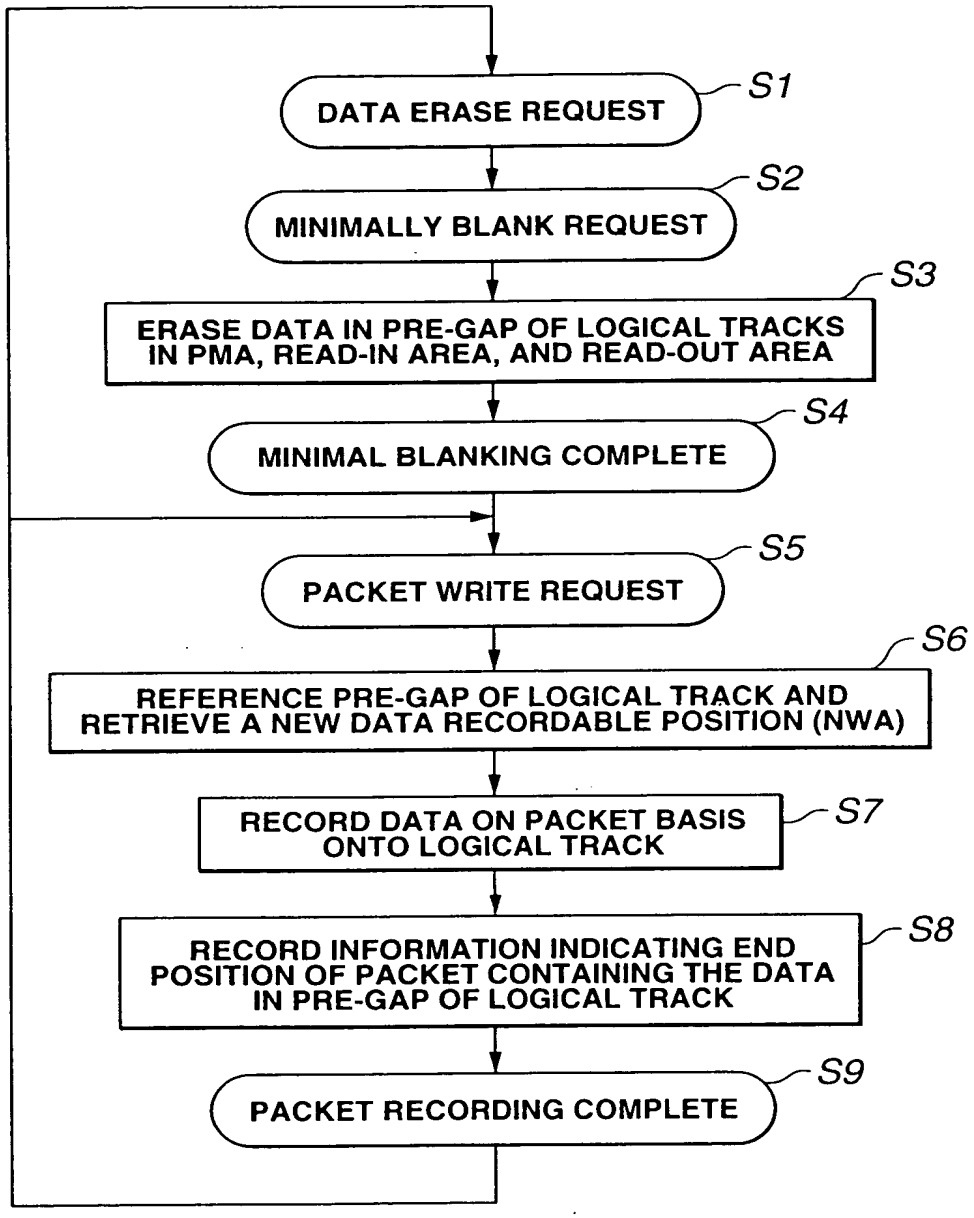

DATA ERASE REQUEST
S1
MINIMALLY BLANK REQUEST
S2
ERASE DATA IN PRE-GAP OF LOGICAL TRACKS IN PMA, READ-IN AREA, AND READ-OUT AREA
S3
MINIMAL BLANKING COMPLETE
S4
PACKET WRITE REQUEST
S5
REFERENCE PRE-GAP OF LOGICAL TRACK AND RETRIEVE A NEW DATA RECORDABLE POSITION (NWA)
S6
RECORD DATA ON PACKET BASIS ONTO LOGICAL TRACK
S7
RECORD INFORMATION INDICATING END POSITION OF PACKET CONTAINING THE DATA IN PRE-GAP OF LOGICAL TRACK
S8
PACKET RECORDING COMPLETE
S9

FIG.8

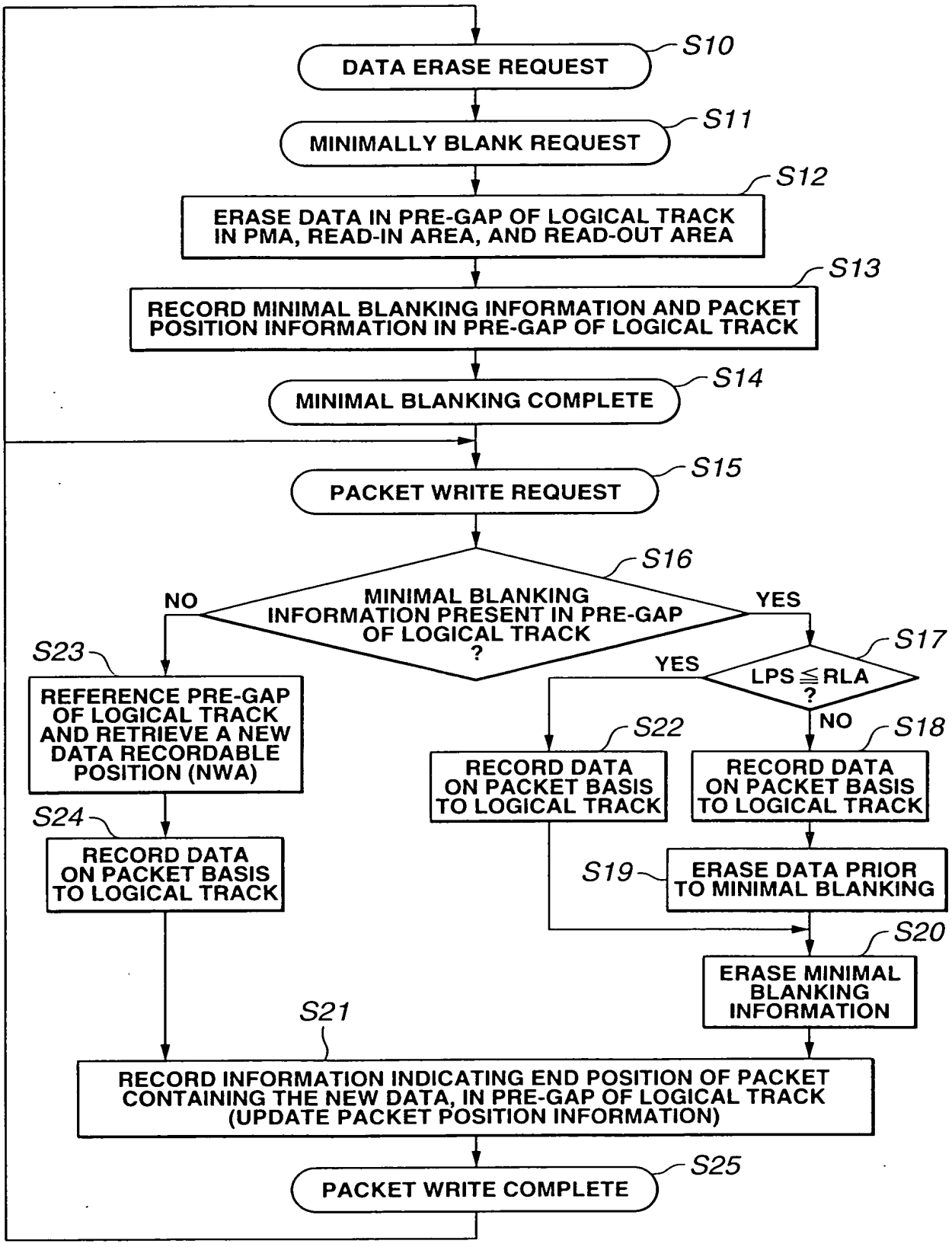

DATA ERASE REQUEST
S10
MINIMALLY BLANK REQUEST
S11
ERASE DATA IN PRE-GAP OF LOGICAL TRACK IN PMA, READ-IN AREA, AND READ-OUT AREA
S12
RECORD MINIMAL BLANKING INFORMATION AND PACKET POSITION INFORMATION IN PRE-GAP OF LOGICAL TRACK
S13
MINIMAL BLANKING COMPLETE
S14
PACKET WRITE REQUEST
S15
MINIMAL BLANKING INFORMATION PRESENT IN PRE-GAP OF LOGICAL TRACK ?
S16
NO
YES
S23
REFERENCE PRE-GAP OF LOGICAL TRACK AND RETRIEVE A NEW DATA RECORDABLE POSITION (NWA)
S24
RECORD DATA ON PACKET BASIS TO LOGICAL TRACK
LPS ≦ RLA ?
S17
YES
NO
S22
RECORD DATA ON PACKET BASIS TO LOGICAL TRACK
S18
RECORD DATA ON PACKET BASIS TO LOGICAL TRACK
S19
ERASE DATA PRIOR TO MINIMAL BLANKING
S20
ERASE MINIMAL BLANKING INFORMATION
S21
RECORD INFORMATION INDICATING END POSITION OF PACKET CONTAINING THE NEW DATA, IN PRE-GAP OF LOGICAL TRACK (UPDATE PACKET POSITION INFORMATION)
PACKET WRITE COMPLETE
S25

FIG.9

CONTENTS

TRACK DESCRIPTOR TABLE	0	54						
	1	44						
	2	49						
	3	01						
	4	50						
	5	00						
	6	01						
	7	04	BYTE CONTENTS		BYTE CONTENTS		BYTE CONTENTS	
TRACK DESCRIPTOR UNITS	8	04	24	01	40	02	56	04
	9	80	25	91	41	91	57	90
	10	FF	26	00	42	00	58	FF
	11	FF	27	00	43	00	59	FF
	12	FF	28	32	44	64	60	FF
	13	00	29	00	45	00	61	00
	14	00	30	00	46	00	62	00
	15	00	31	00	47	00	63	00
	16	00	32	00	48	00	64	00
	17	00	33	00	49	00	65	00
	18	00	34	00	50	00	66	00
	19	00	35	00	51	00	67	00
	20	00	36	00	52	00	68	00
	21	00	37	00	53	00	69	00
	22	00	38	00	54	00	70	00
	23	00	39	00	55	00	71	00
NON-USED BYTES							72	00
							· ·	00
							· ·	00
							2047	00

FIG.10

BYTE/BIT	7	6	5	4	3	2	1	0
0	54th (ASCII "T")							
1	44th (ASCII "D")							
2	49th (ASCII "I")							
3	PRE-GAP LENGTH							
4								
5	Reserved							Current
6	Lowest Track Number							
7	Highest Track Number							
8	Track Number							
9	RECORDING METHOD							
10	(MSB) FIXED-LENGTH PACKET SIZE							
11								
12								(LSB)
13	Reserved (00)							
14	MINIMAL BLANKING INFORMATION							
15	Reserved (00)							
16	(t) PACKET POSITION INFORMATION							
17								
18								
19								(LSB)
20	Reserved (00)							
21	Reserved (00)							
22	Reserved (00)							
23	Reserved (00)							
:								
2047								

FIG.11